CLAIMS:

Sub B1

1. A data processing system which may be situated in a reduced-power mode, comprising a first data processing unit that has access to a memory belonging to the first data processing unit and a second data processing unit that has access to the memory belonging to the first data processing unit,
characterized in that the first data processing unit is arranged for offering the second data processing unit access to the memory belonging to the first data processing unit in a reduced-power mode of the data processing system.

2. A data processing system as claimed in claim 1,
characterized in that the first data processing unit is arranged for offering the second data processing unit access to the memory belonging to the first data processing unit in a period of time in which the reduced-power mode of the data processing system implies a reduced-power mode of the first data processing unit.

3. A data processing system as claimed in claim 1 ~~or 2,~~
characterized in that the first data processing unit is arranged for offering the second data processing unit access to the memory belonging to the first data processing unit when a memory belonging to the second data processing unit is switched off.

4. A system as claimed in claim 1, ~~2 or 3,~~
characterized in that the memory belonging to the first data processing unit forms part of the first data processing unit.

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5. A system as claimed in claim 1, ~~2, 3 or 4,~~
characterized in that the memory belonging to the first data processing unit is a cache memory.

6. A system as claimed in claim 1, ~~2, 3 or 4,~~
characterized in that the first data processing unit is a microprocessor.

7. A system as claimed in claim 1, ~~2 or 3,~~ characterized in that the second data processing unit is a video controller.

Sub B1

8. A data processing unit having access to a memory belonging to the data processing unit which data processing unit may be situated in a reduced-power mode, characterized in that the data processing unit is arranged for offering access in the reduced-power mode to the memory belonging to the data processing unit.

add B1